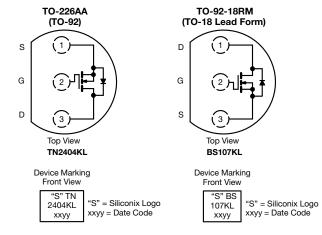


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N-Channel 240 V (D-S) MOSFET

PRODUCT SUMMARY						
Part Number	V _{DS} (V)	R _{DS(on)} (Ω)	V _{GS(th)} (A)	I _D (A)	Q _g (Typ.)	
TN2404K				0.2		
TN2404K, BS107KL	240	4 at V _{GS} = 10 V	0.8 to 2	0.3	4.87 nC	



FEATURES

Low On-Resistance: 4 Ω

Secondary Breakdown Free: 260 V

Low Power/Voltage Driven

Low Input and Output Leakage

Excellent Thermal Stability

Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

COMPLIANT HALOGEN FREE

APPLICATIONS

- · High-Voltage Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Transistors, etc.
- Telephone Mute Switches, Ringer Circuits
- Power Supply, Converters
- Motor Control

TO-236 (SOT-23) TN2404K

BENEFITS

- Low Offset Voltage
- **Full-Voltage Operation**
- Easily Driven Without Buffer
- - Low Error Voltage
- K1 = Part Number Code for TN2404K y = Year Code w = Week Code I = Lot Traceability

Marking Code: K1ywl

No High-Temperature "Run-Away"

ORDRING INFORMATION					
Standard Partnumber	Ordering Part Number	Option			
TN2404K	TN2404K-T1-E3	Lead (Pb) free			
11\2404K	TN2404K-T1-GE3	Lead (Pb) free and Halogen free			
TN2404KL	TN2404KL-TR1-E3	With Tape and Reel			
BS107KL	BS107KL-TR1-E3	Spool Option			

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)							
Parameter		Symbol	TN2404K	04K TN2404KL/BS107KL			
Drain-Source Voltage		V_{DS}	24	V			
Gate-Source Voltage		V_{GS}	± 20		v		
Continuous Drain Current (T, = 150 °C)	T _A = 25 °C	- I _D	0.2	0.3			
Continuous Brain Gunerit (1) = 130 O)	T _A = 70 °C		0.16	0.25	Α		
Pulsed Drain Current (t = 300 μs)		I _{DM}	0.8	1.4			
Maximum Power Dissipation	T _A = 25 °C	P _D	0.36	0.8	W		
Maximum r ower bissipation	T _A = 70 °C	' ^D	0.23	0.51			
Thermal Resistance Junction-to-Ambient		R _{thJA}	350 ^b	156	°C/W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 1	°C			

a. Pulse width limited by maximum junction temperature.

b. Surface mounted on an FR4 board.

TN2404K/TN2404KL/BS107KL

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SPECIFICATIONS (T _A = 25 °C, unless otherwise noted)							
Parameter	Symphol	Test Conditions	Limits				
raiametei	Symbol	rest Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$	240	257		V	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8	1.65	2	v	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	lana	V _{DS} = 192 V, V _{GS} = 0 V	1		1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 192 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On Olate Dunin Comment	1	V _{DS} = 10 V, V _{GS} = 10 V	0.8			Λ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 10 V, V _{GS} = 4.5 V	0.5			Α	
		$V_{GS} = 10 \text{ V}, I_D = 0.3 \text{ A}$		2.2	4		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 0.2 \text{ A}$		2.3	4	Ω	
		$V_{GS} = 2.5 \text{ V}, I_D = 0.1 \text{ A}$		2.4	6		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 0.3 \text{ A}$		1.6		S	
Diode Forward Voltage	V _{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 0.3 \text{ A}$		0.8	1.2	V	
Dynamic ^b	·						
Total Gate Charge	Qg			4.87	8		
Gate-Source Charge	Q _{gs}	$V_{DS} = 192 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		0.56		nC	
Gate-Drain Charge	Q _{gd}			1.53			
Turn-On Delay Time	t _{d(on)}			5	10		
Rise Time	t _r	V_{DD} = 60 V, R_L = 200 Ω		12	20	ne	
Turn-Off Delay Time t _{d(off)}		$I_D \cong 0.3 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 25 \Omega$		35	60	- ns	
Fall Time	t _f			16	25		

Notes:

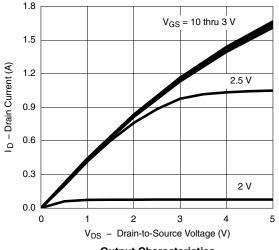
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

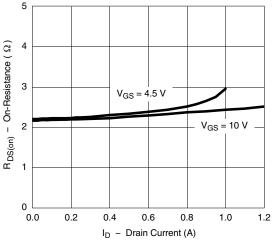
b. Guaranteed by design, not subject to production testing.



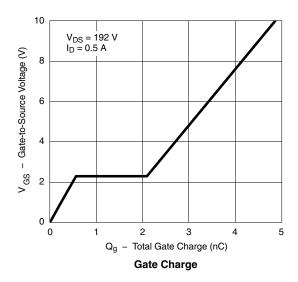
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

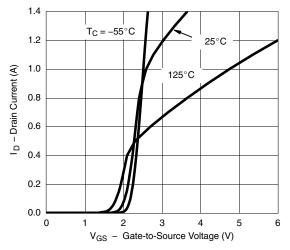


Output Characteristics

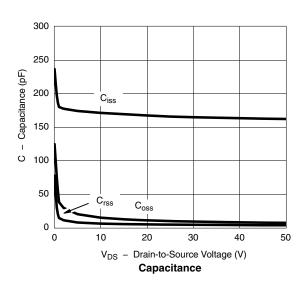


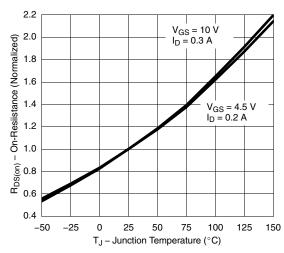
On-Resistance vs. Drain Current





Transfer Characteristics





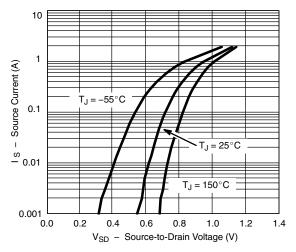
On-Resistance vs. Junction Temperature

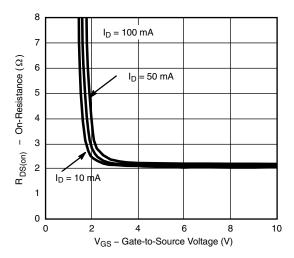
TN2404K/TN2404KL/BS107KL

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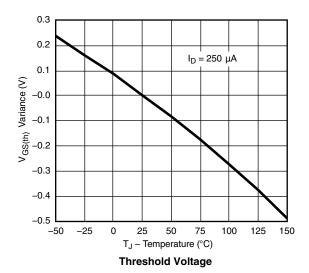
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





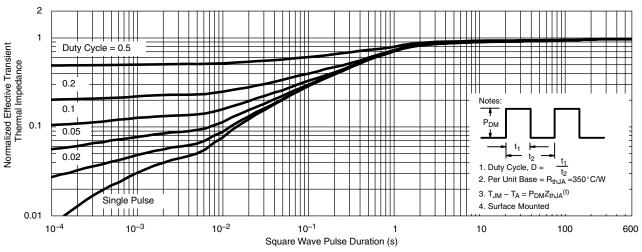
Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

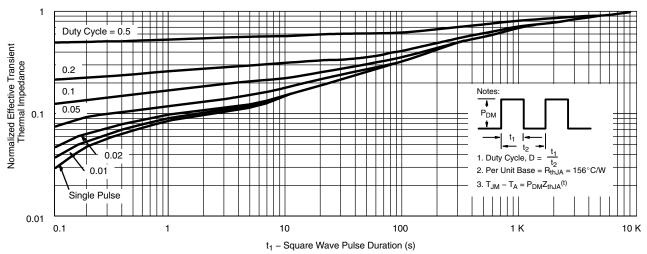




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-236, TN2404K only)



Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-226AA, TN2404KL and TO-92-18RM, BS107KL only)

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72225.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
Dim	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.9	0.95 BSC		4 Ref	
e ₁	1.9	0 BSC	0.074	8 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.5	50 Ref	0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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