Vishay Siliconix

Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	60				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.068				
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.075				
I _D (A)	4.3				
Configuration	Single				
Package	SOT-23				

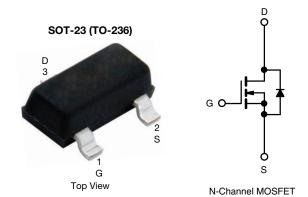
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



Marking Code: 8Z

ABSOLUTE MAXIMUM RATING	S ($T_C = 25 ^{\circ}C$, unless	otherwise noted	d)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	60	.,	
Gate-Source Voltage		V _{GS} ± 20			
Continuous Drain Current	T _C = 25 °C		4.3	A	
	T _C = 125 °C	- I _D	2.5		
Continuous Source Current (Diode Conduc	tion)	I _S	3.8		
Pulsed Drain Current ^a		I _{DM}	17		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	12		
Single Pulse Avalanche Energy	L = 0.1 min	E _{AS}	7	mJ	
Martin of Broad Bireline 2	T _C = 25 °C	D	3	W	
Maximum Power Dissipation ^a	T _C = 125 °C	P_{D}	1	VV	
Operating Junction and Storage Temperatu	re Range	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount b	R _{thJA}	166	°C/W		
Junction-to-Foot (Drain)		R _{thJF}	50	C/VV		

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2.0	2.5		
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		V _{GS} = 0 V	V _{DS} = 60 V	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μA	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 \text{ V}$	10	-	-	Α	
		V _{GS} = 10 V	I _D = 2.4 A	-	0.057	0.068	Ω	
Due in Course On Chata Basistana 3		V _{GS} = 10 V	I _D = 2.4 A, T _J = 125 °C	-	-	0.114		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 2.4 A, T _J = 175 °C	-	-	0.147		
		V _{GS} = 4.5 V	I _D = 2.3 A	-	0.062	0.075		
Forward Transconductance b	9fs	V _{DS} = 15 V, I _D = 2.4 A		-	10	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = 30 V, f = 1 MHz	-	440	550	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	50	63		
Reverse Transfer Capacitance	C _{rss}	1		-	22	26		
Total Gate Charge ^c	Qg			-	7.6	12		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 30 \text{ V}, I_D = 2.4 \text{ A}$	-	1.1	-	nC	
Gate-Drain Charge ^c	Q _{gd}	1		-	2.7	-		
Gate Resistance	R _g	f = 1 MHz		1.3	2.7	4.1	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	6	10		
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V}, \text{ R}_L = 12.5 \Omega$ $I_D \cong 2.4 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		-	20	30	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	14	21		
Fall Time ^c	t _f			-	18	30		
Source-Drain Diode Ratings and Chara	acteristics b	•						
Pulsed Current a	I _{SM}			_	_	17	Α	
i dioca odiront	- SIVI							

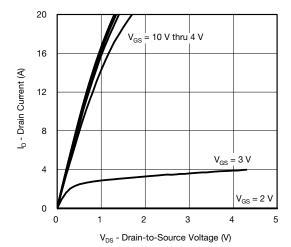
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,\,duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

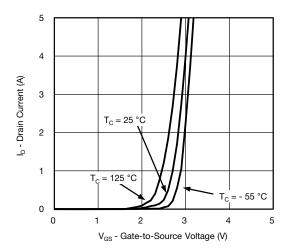
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



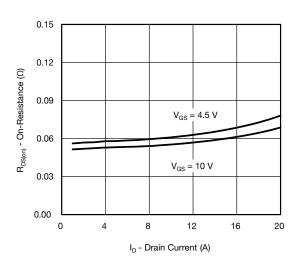
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



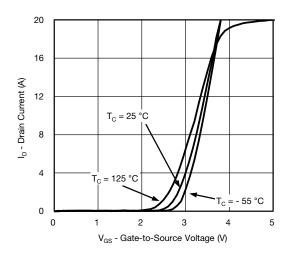
Output Characteristics



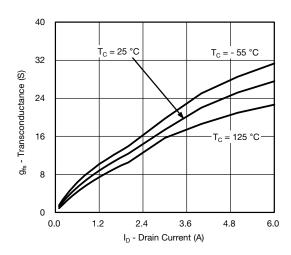
Transfer Characteristics



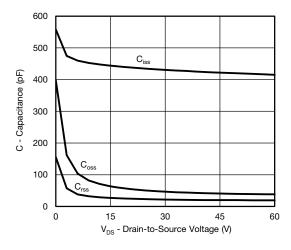
On-Resistance vs. Drain Current



Transfer Characteristics

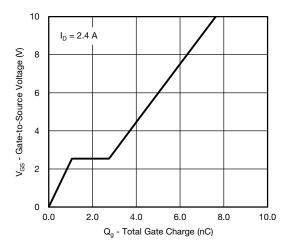


Transconductance

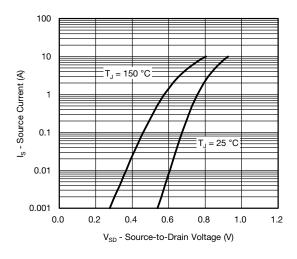




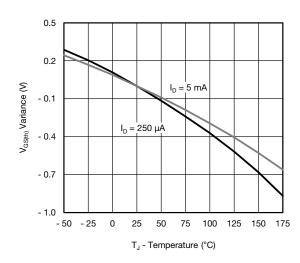
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



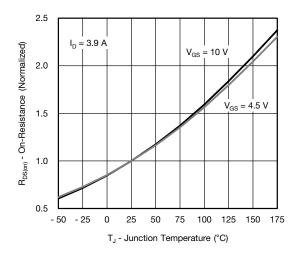
Gate Charge



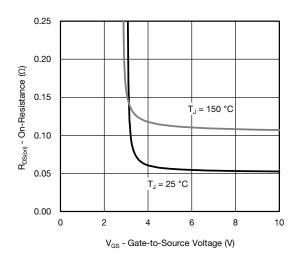
Source Drain Diode Forward Voltage



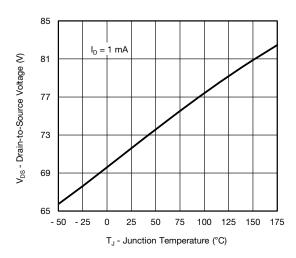
Threshold Voltage



On-Resistance vs. Junction Temperature



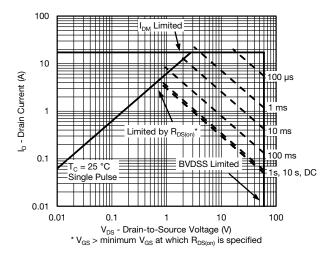
On-Resistance vs. Gate-to-Source Voltage



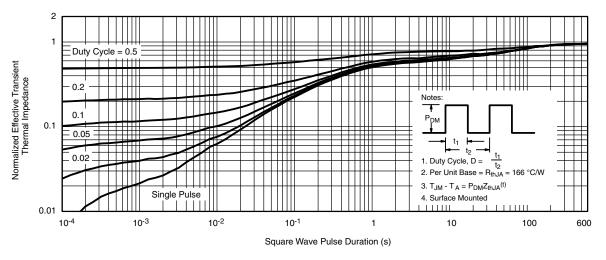
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



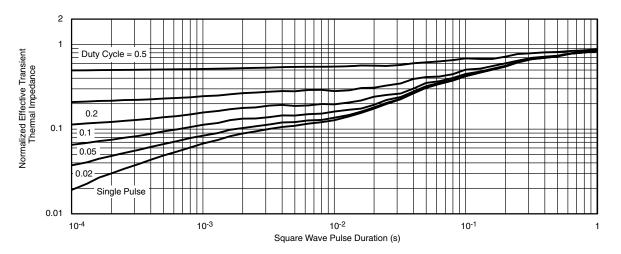
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62913.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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