

## Vishay Semiconductors

**HALOGEN** 

FREE

# **Small Signal Fast Switching Diodes**



### **FEATURES**

- Silicon epitaxial planar diode
- · Saving space
- · Hermetic sealed parts
- Fits onto SOD-323 / SOT-23 footprints
- Electrical data identical with the devices 1N4148 and 1N4448 respectively
- MicroMELF package
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



· Extreme fast switches





#### **MECHANICAL DATA**

Case: MicroMELF

Weight: approx. 12 mg

Cathode band color: black

Packaging codes / options:

TR3/10K per 13" reel (8 mm tape), 10K/box TR/2.5K per 7" reel (8 mm tape), 12.5K/box

PARTS TABLE						
PART	TYPE DIFFERENTIATION	ORDERING CODE	CIRCUIT CONFIGURATION	REMARKS		
MCL4148	$V_{RRM} = 100 \text{ V}, V_F \text{ at } I_F 50 \text{ mA} = 1 \text{ V}$	MCL4148-TR3 or MCL4148-TR	Single	Tape and reel		
MCL4448	$V_{RRM} = 100 \text{ V}, V_F \text{ at } I_F 100 \text{ mA} = 1 \text{ V}$	MCL4448-TR3 or MCL4448-TR	Single	Tape and reel		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Repetitive peak reverse voltage		$V_{RRM}$	100	V	
Peak forward surge current	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	2	Α	
Repetitive peak forward current		I <sub>FRM</sub>	450	mA	
Forward continuous current		I <sub>F</sub>	200	mA	
Average forward current	V <sub>R</sub> = 0 V	I <sub>F(AV)</sub>	150	mA	
Power dissipation		P <sub>tot</sub>	500	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air	Mounted on epoxy-glass hard tissue, Fig. 5, 35 µm copper clad, 0.9 mm <sup>2</sup> copper area per electrode	$R_{thJA}$	500	K/W	
Junction temperature		T <sub>j</sub>	175	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +175	°C	

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I <sub>F</sub> = 5 mA	MCL4448	$V_{F}$	0.620		0.720	V
Forward voltage	I <sub>F</sub> = 50 mA	MCL4148	$V_{F}$		0.860	1	V
	I <sub>F</sub> = 100 mA	MCL4448	$V_{F}$		0.930	1	V
	V <sub>R</sub> = 20 V		I <sub>R</sub>			25	nA
Reverse current	V <sub>R</sub> = 20 V, T <sub>j</sub> = 150 °C		I <sub>R</sub>			50	μΑ
	V <sub>R</sub> = 75 V		I <sub>R</sub>			5	μA
Breakdown voltage	$I_R = 100 \mu A, t_p/T = 0.01,$ $t_p = 0.3 \text{ ms}$		V <sub>(BR)</sub>	100			V
Diode capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz,}$ $V_{HF} = 50 \text{ mV}$		C <sub>D</sub>			4	pF
Rectification efficiency	V <sub>HF</sub> = 2 V, f = 100 MHz		$\eta_r$	45			%
Doverse vecesses time	$I_F = I_R = 10 \text{ mA},$ $i_R = 1 \text{ mA}$		t <sub>rr</sub>			8	no
Reverse recovery time	$I_F = 10 \text{ mA}, V_R = 6 \text{ V},$ $I_R = 0.1 \text{ x } I_R, R_L = 100 \Omega$		t <sub>rr</sub>			4	ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

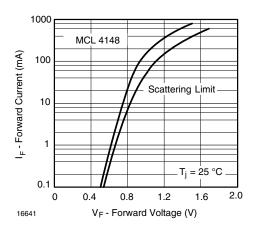


Fig. 1 - Reverse Current vs. Junction Temperature

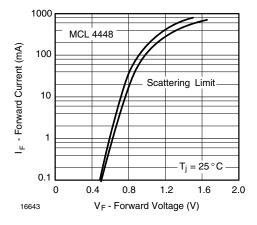


Fig. 2 - Forward Current vs. Forward Voltage

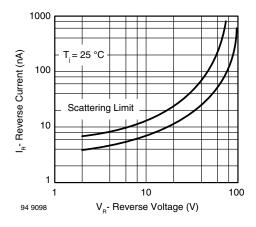


Fig. 3 - Reverse Current vs. Reverse Voltage

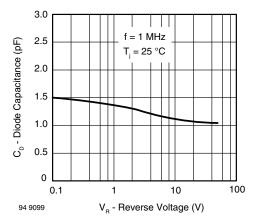


Fig. 4 - Diode Capacitance vs. Reverse Voltage

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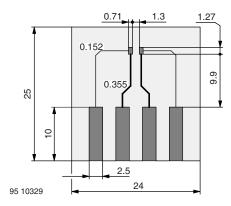
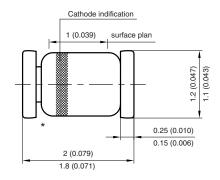
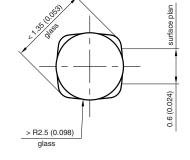


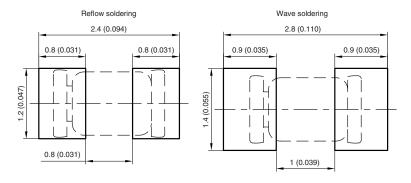
Fig. 5 - Board for R<sub>thJA</sub> definition (in mm)

### PACKAFE DIMENSIONS in millimeters (inches): MicroMELF





#### Foot print recommendation:



Created - Date: 26.July.1996 Rev. 13 - Date: 07.June.2006 Document no.:6.560-5007.01-4 96 12072

<sup>\*</sup> The gap between plug and glass can be either on cathode or anode side



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Vishay

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