

超小型、低输入电压、低 $R_{\text{导通}}$ 负载开关

查询样品: [TPS22908](#)

特性

- 低输入电压: **1.0V 至 3.6V**
- 超低导通状态电阻 ($R_{\text{导通}}$)
- 典型 $R_{\text{导通}}$ 值
 - 在输入电压 (V_{IN}) = 3.6V 时, $R_{\text{导通}}$ = **28m Ω**
 - V_{IN} = 2.5V 时, $R_{\text{导通}}$ = **33m Ω**
 - V_{IN} = 1.8V 时, $R_{\text{导通}}$ = **42m Ω**
 - V_{IN} = 1.2V 时, $R_{\text{导通}}$ = **70m Ω**
- **1A** 最大持续开关电流
- 最大静态电流 = **1 μ A**
- 最大关断电流 = **1 μ A**
- 低控制输入阈值可实现低压逻辑的使用
- 受控制的转换率以避免浪涌电流
- 超小型四端子晶圆级芯片封装 (**WCSP**)
 - 标称尺寸 - 细节请见附录
 - **0.9mm $\times$ 0.9mm**
 - 焊球间距 **0.5mm**, 高度 **0.6mm**
- 快速输出放电 (**QOD**)

应用范围

- 电池供电类设备
- 便携式工业设备
- 便携式医疗设备
- 便携式媒体播放器
- 销售点终端
- 全球卫星定位 (**GPS**) 设备
- 数码摄像机
- 便携式仪表
- 智能电话/平板电脑

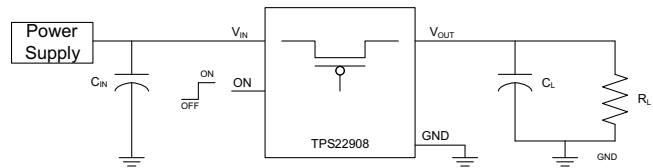


图 1. 典型应用

说明

TPS22908 是一款具有受控接通功能的超小型、低 $R_{\text{导通}}$ 负载开关。此器件包括一个 P 通道金属氧化物半导体场效应晶体管 (MOSFET)，此晶体管可在 1.0V 至 3.6V 的输入电压范围内运行。此开关由一个开/关输入 (ON) 控制，此输入能够与低电压控制信号直接对接。

TPS22908 采用一个节省空间的 4 端子晶圆级芯片封装 (WCSP)，此封装的焊球间距为 0.5mm (YZT)。器件在自然通风环境下的额定运行温度范围为 -40°C 至 85°C。

订购信息

封装和订购信息，请参见本文档末尾的封装选项附录。

特性列表

器件	$V_{\text{IN}} = 3.6\text{V}$ 时的 $R_{\text{导通}}$ (典型值)	$V_{\text{IN}} = 3.6\text{V}$ 时的上升时间 (典型值)	快速输出放电 ⁽¹⁾	最大电流	使能
TPS22908	28m Ω	105 μ s	支持	1A	高电平有效

(1) 通过一个 80 Ω 的电阻器，此特性可将开关的输出放电至接地水平，从而防止此输出悬空。



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of the Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

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English Data Sheet: [SLVSB17](#)

TPS22908

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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

ABSOLUTE MAXIMUM RATINGS

Over operating free-air temperature range (unless otherwise noted)⁽¹⁾

			VALUE	UNIT ⁽²⁾
V _{IN}	Supply voltage range		–0.3 to 4	V
V _{OUT}	Output voltage range		–0.3 to (V _{IN} + 0.3)	V
V _{ON}	Input voltage range		–0.3 to 4	V
I _{MAX}	Maximum Continuous Switch Current for V _{IN} >= 1.2V		1	A
	Maximum Continuous Switch Current at V _{IN} = 1.0V		0.6	
T _A	Operating free-air temperature range ⁽³⁾		–40 to 85	°C
T _J	Maximum junction temperature		125	°C
T _{STG}	Storage temperature range		–65 to 150	°C
T _{LEAD}	Maximum lead temperature (10-s soldering time)		300	°C
ESD	Electrostatic discharge protection	Human-Body Model (HBM)	2000	V
		Charged-Device Model (CDM)	1000	

- (1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltage values are with respect to network ground terminal.
- (3) In applications where high power dissipation and/or poor package thermal resistance is present, the maximum ambient temperature may have to be derated. Maximum ambient temperature [T_{A(max)}] is dependent on the maximum operating junction temperature [T_{J(max)}], the maximum power dissipation of the device in the application [P_{D(max)}], and the junction-to-ambient thermal resistance of the part/package in the application (θ_{JA}), as given by the following equation: T_{A(max)} = T_{J(max)} – (θ_{JA} × P_{D(max)})

THERMAL INFORMATION

THERMAL METRIC ⁽¹⁾⁽²⁾		TPS22908	UNITS
		YZT (4 PINS)	
θ _{JA}	Junction-to-ambient thermal resistance	188	°C/W
θ _{JC(top)}	Junction-to-case(top) thermal resistance	2	
θ _{JB}	Junction-to-board thermal resistance	33	
ψ _{JT}	Junction-to-top characterization parameter	9.1	
ψ _{JB}	Junction-to-board characterization parameter	33	
θ _{JC(bottom)}	Junction-to-case(bottom) thermal resistance	N/A	

- (1) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, [SPRA953](#)
- (2) For thermal estimates of this device based on PCB copper area, see the TI PCB Thermal Calculator.

RECOMMENDED OPERATING CONDITIONS

			MIN	MAX	UNIT
V _{IN}	Input voltage range		1.0	3.6	V
V _{ON}	ON voltage range		0	3.6	V
V _{OUT}	Output voltage range		0	V _{IN}	V
V _{IH}	High-level input voltage, ON		0.85	3.6	V
V _{IL}	Low-level input voltage, ON		0	0.4	V
C _{IN}	Input capacitor		1 ⁽¹⁾		μF

- (1) Refer to application section.

ELECTRICAL CHARACTERISTICS

Unless otherwise noted the specification applies over the operating ambient temp $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$. Typical values are for $V_{IN} = 3.6\text{ V}$, and $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

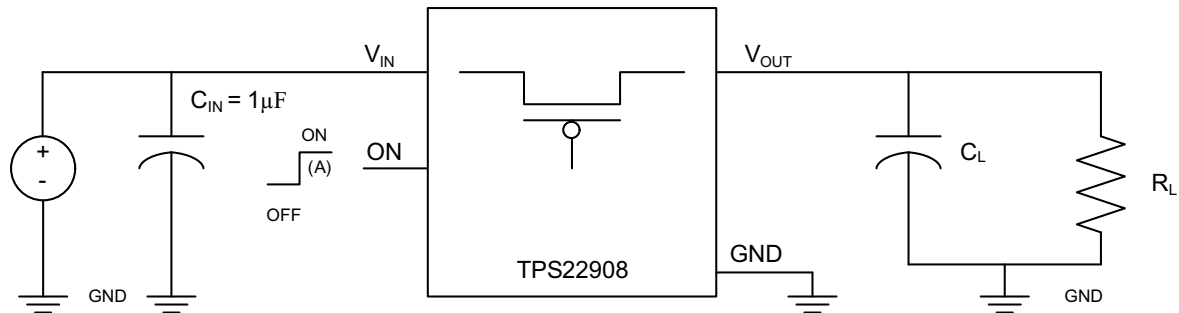
PARAMETER		TEST CONDITIONS	T_A	MIN	TYP	MAX	UNIT
POWER SUPPLIES AND CURRENTS							
I_{IN}	Quiescent current	$I_{OUT} = 0\text{ mA}$, $V_{IN} = V_{ON}$	Full		0.19	1	μA
$I_{IN(OFF)}$	OFF-state supply current	$V_{ON} = 0\text{ V}$, $V_{OUT} = \text{Open}$	Full		0.12	1	μA
$I_{IN(LEAK)}$	OFF-state supply current	$V_{ON} = 0\text{ V}$, $V_{OUT} = 0\text{ V}$	Full		0.12	1	μA
I_{ON}	ON pin input leakage current	$V_{ON} = 1.1\text{ V to }3.6\text{ V}$	Full		0.01	0.1	μA
RESISTANCE AND SWITCH CHARACTERISTICS							
R_{ON}	ON-state resistance	$I_{OUT} = -200\text{ mA}$	$V_{IN} = 3.6\text{ V}$	25°C	28.2	32.1	$\text{m}\Omega$
				Full		34.9	
			$V_{IN} = 2.5\text{ V}$	25°C	33.1	37.5	$\text{m}\Omega$
				Full		40.6	
			$V_{IN} = 1.8\text{ V}$	25°C	41.5	50.3	$\text{m}\Omega$
				Full		54.0	
			$V_{IN} = 1.2\text{ V}$	25°C	69.7	87.3	$\text{m}\Omega$
				Full		91.2	
			$V_{IN} = 1.0\text{ V}$	25°C	112	155	$\text{m}\Omega$
				Full		156	
R_{PD}	Output pulldown resistance	$V_{IN} = 3.3\text{ V}$, $V_{ON} = 0\text{ V}$, $I_{OUT} = 30\text{ mA}$	25°C		80	100	Ω

TPS22908

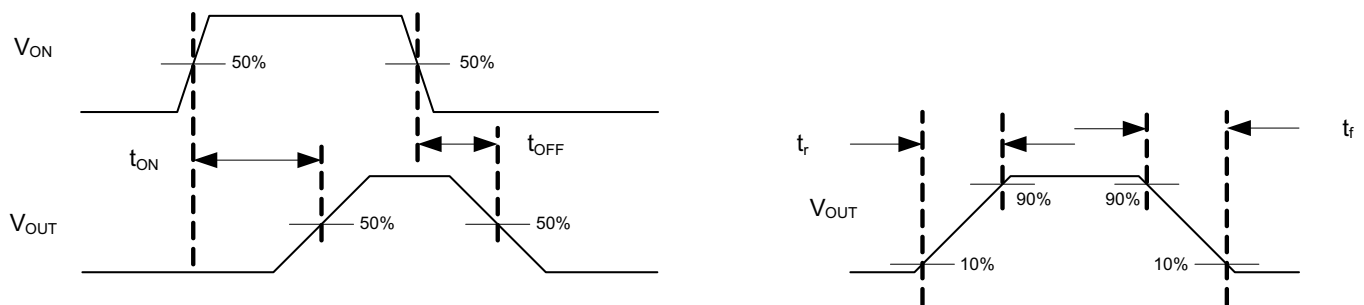
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SWITCHING CHARACTERISTIC MEASUREMENT INFORMATION



TEST CIRCUIT



t_{ON}/t_{OFF} WAVEFORMS

A. Rise and fall times of the control signal is 100 ns.

Figure 2. Test Circuit and t_{ON}/t_{OFF} Waveforms

SWITCHING CHARACTERISTICS

PARAMETER		TEST CONDITION	TPS22908			UNIT
			MIN	TYP	MAX	
V _{IN} = 3.6 V, T _A = 25°C (unless otherwise noted)						
t _{ON}	Turn-ON time	R _L = 10 Ω, C _L = 0.1 μF	110			μs
t _{OFF}	Turn-OFF time	R _L = 10 Ω, C _L = 0.1 μF	5			
t _R	V _{OUT} Rise time	R _L = 10 Ω, C _L = 0.1 μF	105			
t _F	V _{OUT} Fall time	R _L = 10 Ω, C _L = 0.1 μF	2			
V _{IN} = 1.0 V, T _A = 25°C (unless otherwise noted)						
t _{ON}	Turn-ON time	R _L = 10 Ω, C _L = 0.1 μF	493			μs
t _{OFF}	Turn-OFF time	R _L = 10 Ω, C _L = 0.1 μF	7			
t _R	V _{OUT} Rise time	R _L = 10 Ω, C _L = 0.1 μF	442			
t _F	V _{OUT} Fall time	R _L = 10 Ω, C _L = 0.1 μF	2			

FUNCTIONAL BLOCK DIAGRAM and PIN DESCRIPTIONS

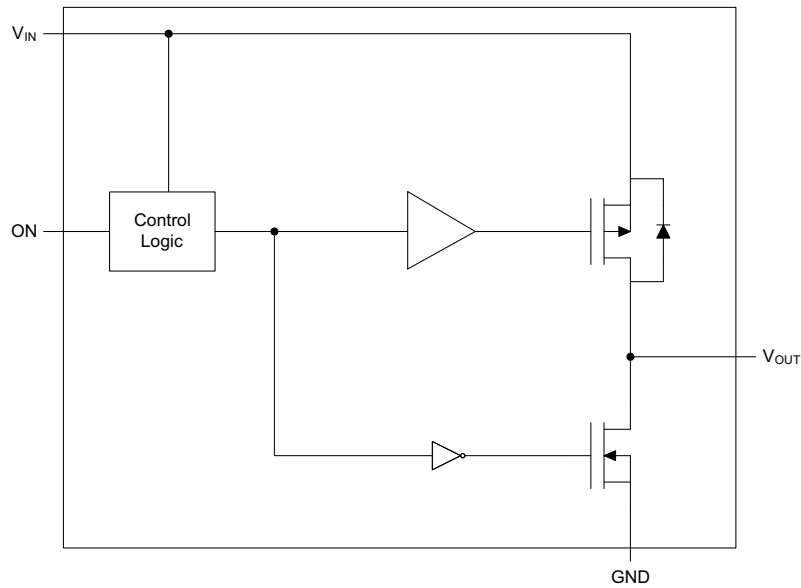


Figure 3. Functional Block Diagram

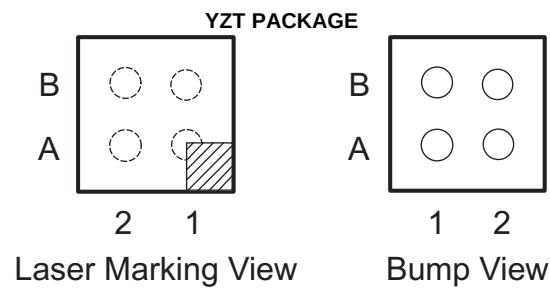


Table 1. FUNCTIONAL TABLE

ON	V_{IN} to V_{OUT}	V_{OUT} to GND
L	Off	On
H	On	Off

PIN DESCRIPTIONS

TPS22908	PIN NAME	DESCRIPTION
YZT		
B2	ON	Switch control input, active high. Do not leave floating.
B1	GND	Ground
A2	V_{IN}	Switch input, bypass capacitor recommended for minimizing V_{IN} dip. See Application Information.
A1	V_{OUT}	Switch output

TYPICAL DC CHARACTERISTICS

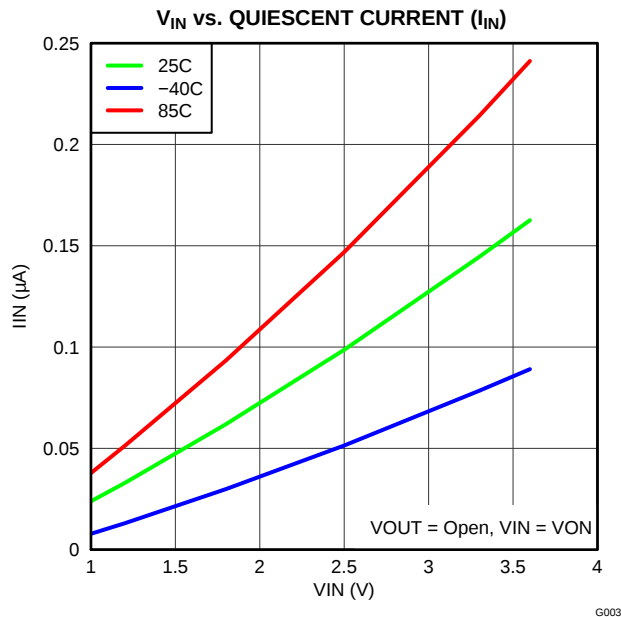


Figure 4.

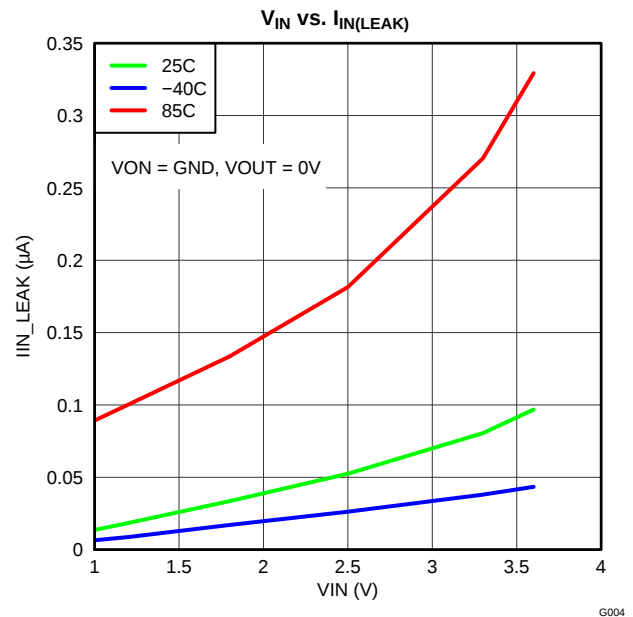


Figure 5.

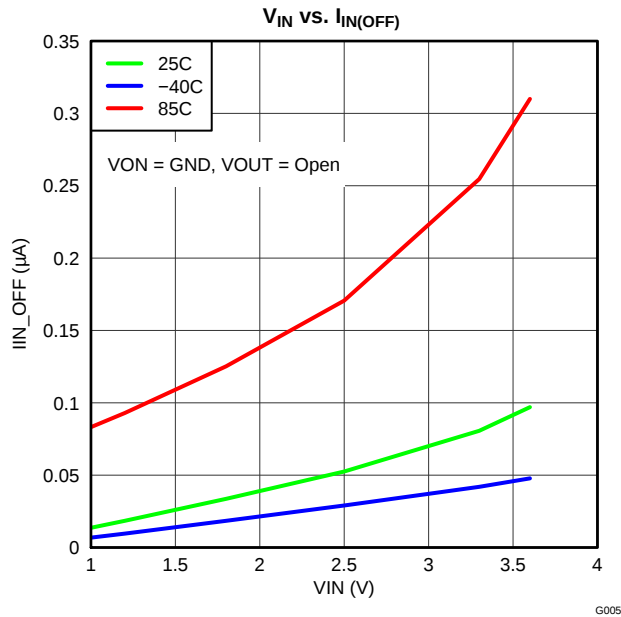


Figure 6.

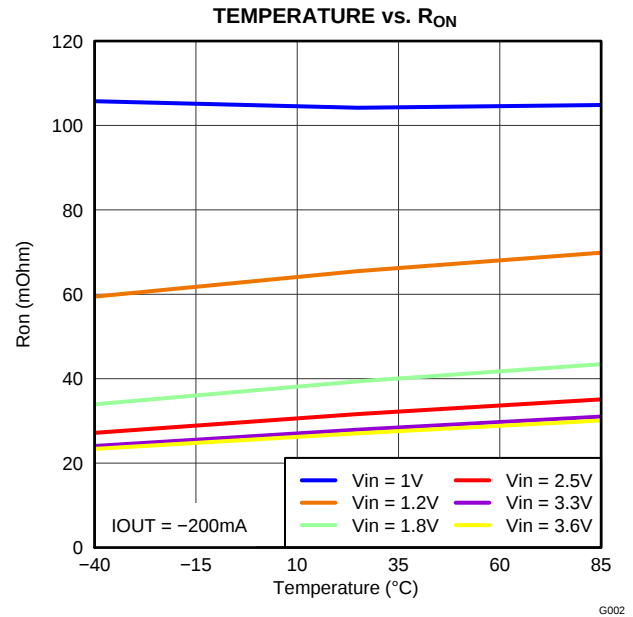


Figure 7.

TYPICAL DC CHARACTERISTICS (continued)

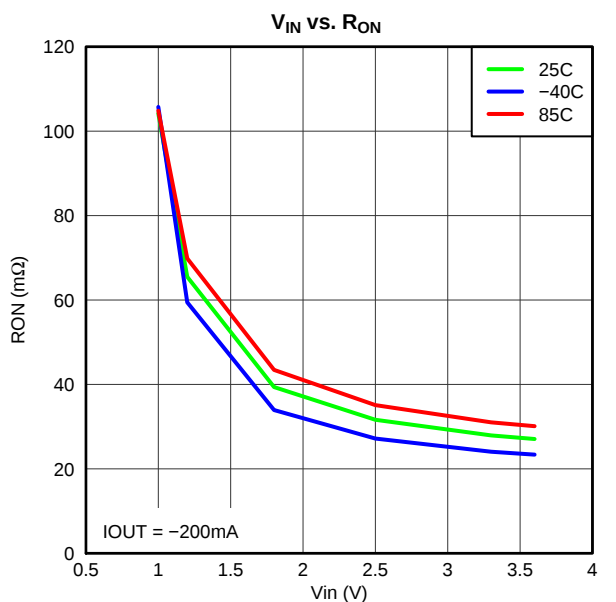


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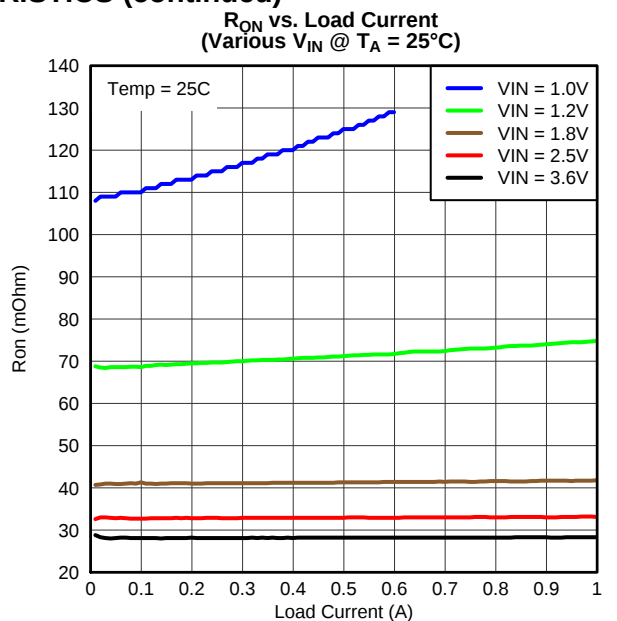


Figure 9.

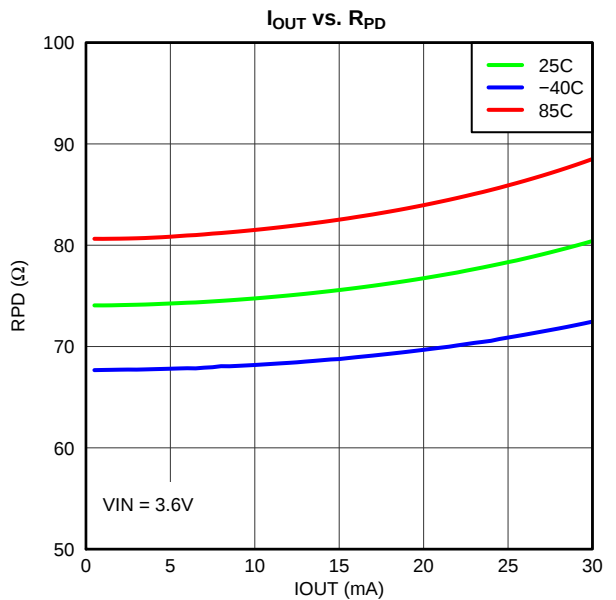


Figure 10.

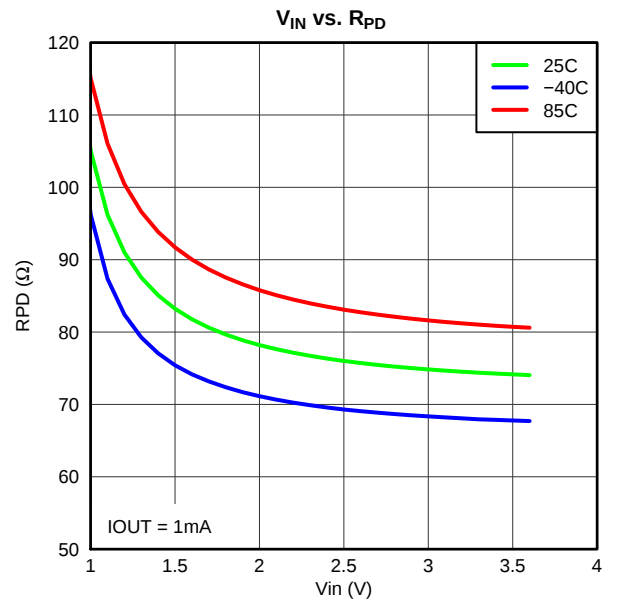


Figure 11.

TYPICAL DC CHARACTERISTICS (continued)

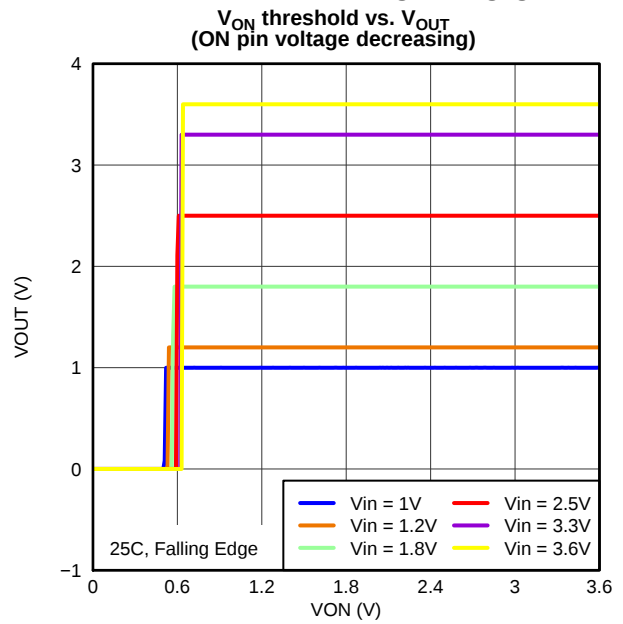


Figure 12.

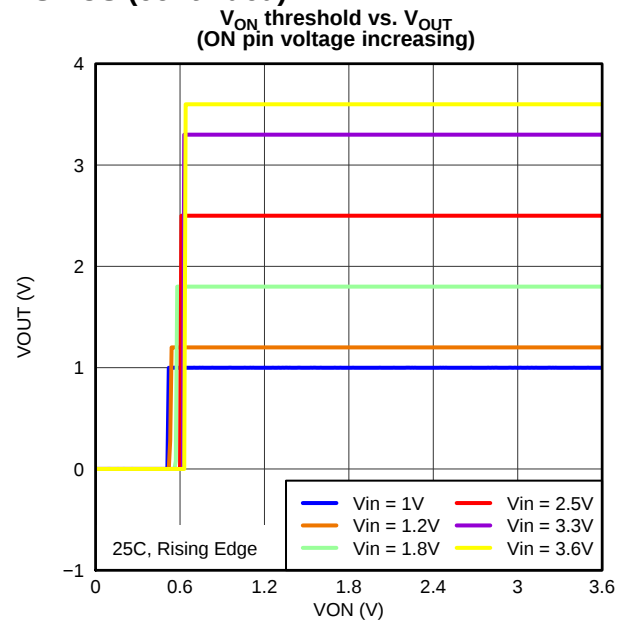


Figure 13.

TYPICAL SWITCHING CHARACTERISTICS

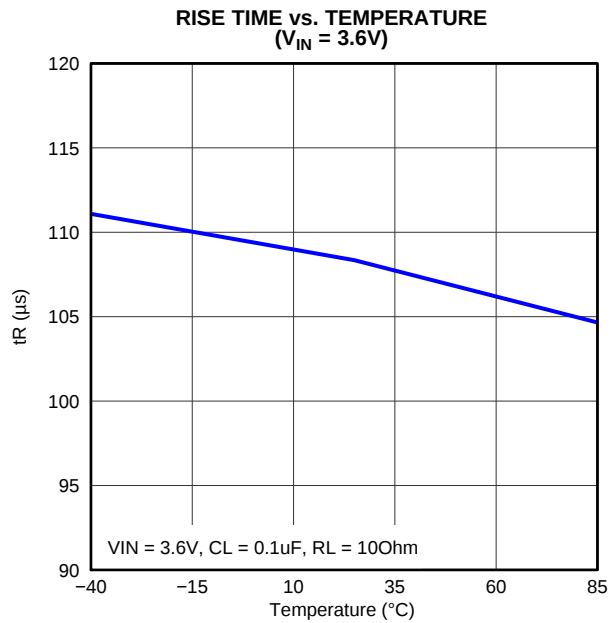


Figure 14.

G013

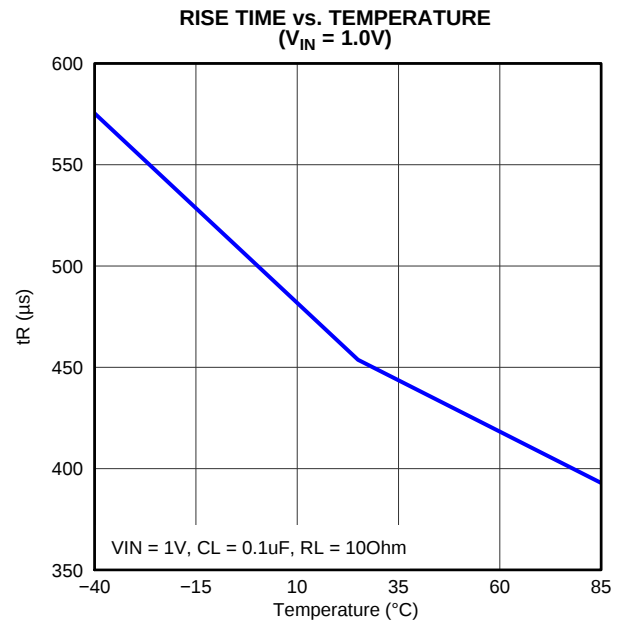


Figure 15.

G018

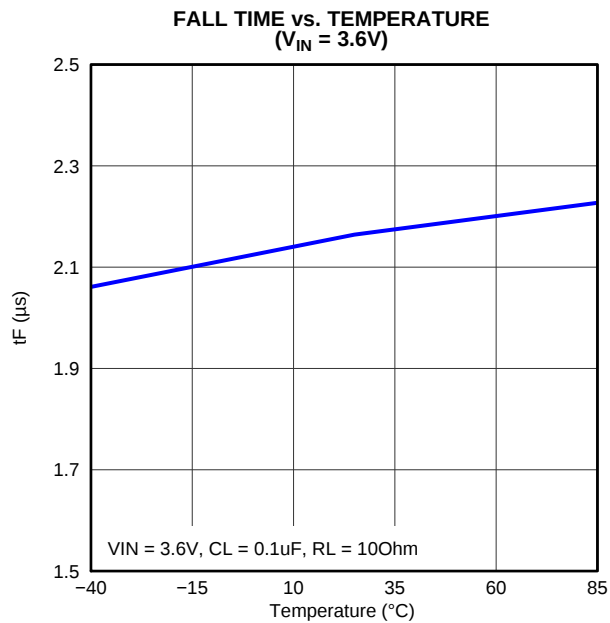


Figure 16.

G024

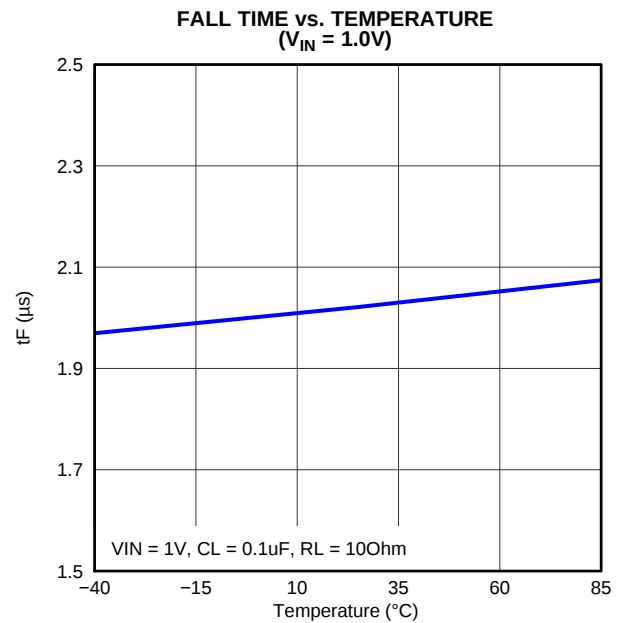
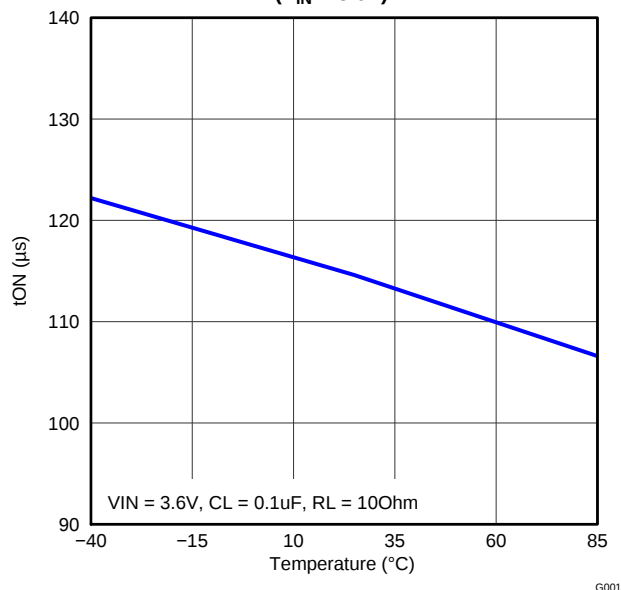
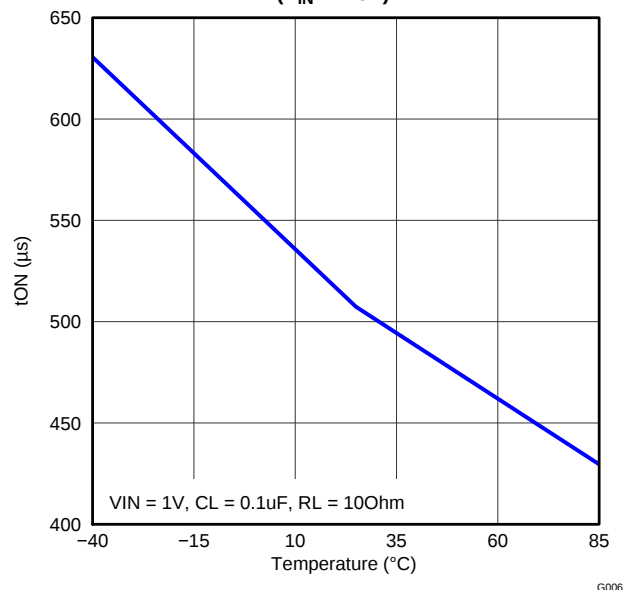


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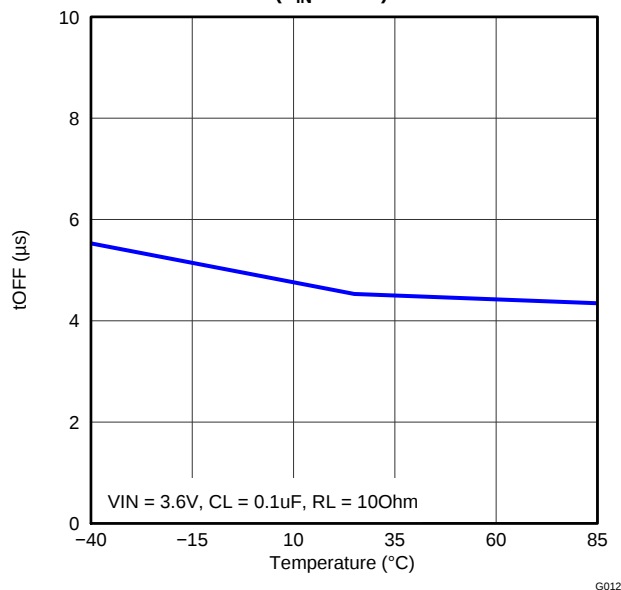
G019

TYPICAL SWITCHING CHARACTERISTICS (continued)
**TURN-ON TIME vs. TEMPERATURE
($V_{IN} = 3.6V$)**

Figure 18.

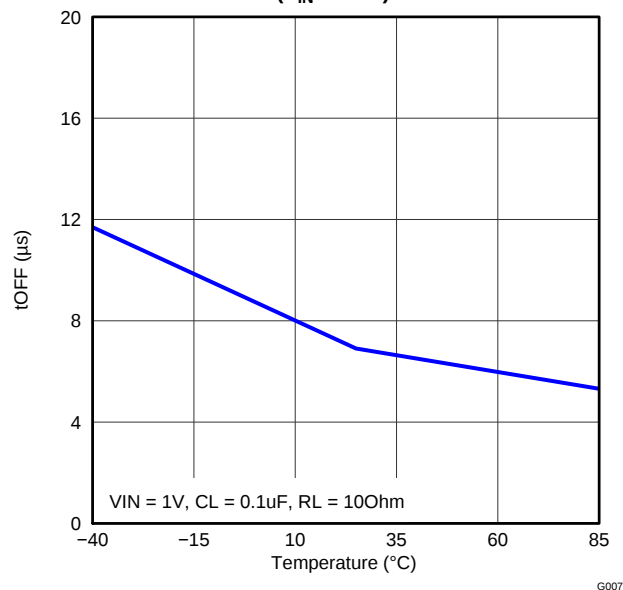
G001

**TURN-ON TIME vs. TEMPERATURE
($V_{IN} = 1.0V$)**

Figure 19.

G006

**TURN-OFF TIME vs. TEMPERATURE
($V_{IN} = 3.6V$)**

Figure 20.

G012

**TURN-OFF TIME vs. TEMPERATURE
($V_{IN} = 1.0V$)**

Figure 21.

G007

TYPICAL SWITCHING CHARACTERISTICS (continued)

RISE TIME vs. V_{IN}
($C_L = 0.1\mu F$)

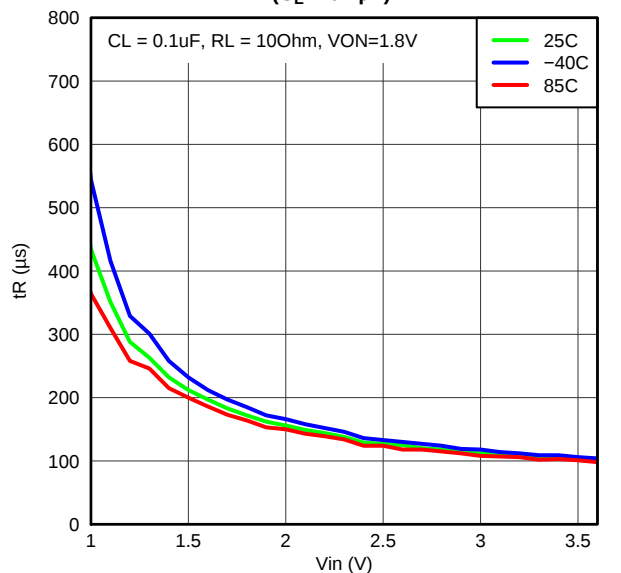


Figure 22.

RISE TIME vs. V_{IN}
($C_L = 1\mu F$)

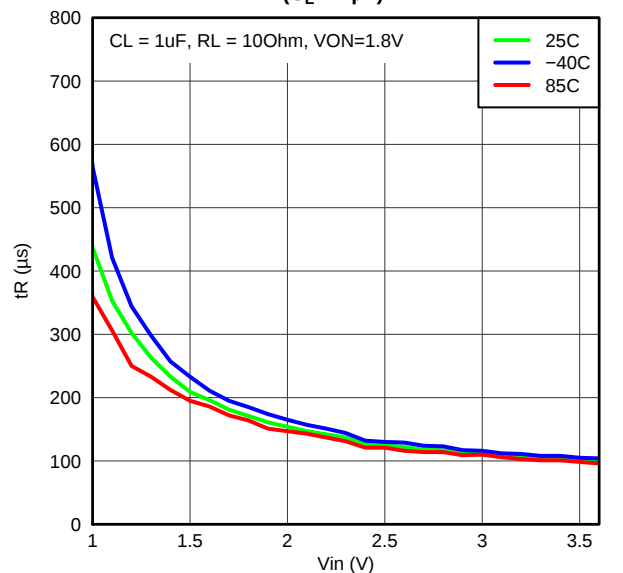


Figure 23.

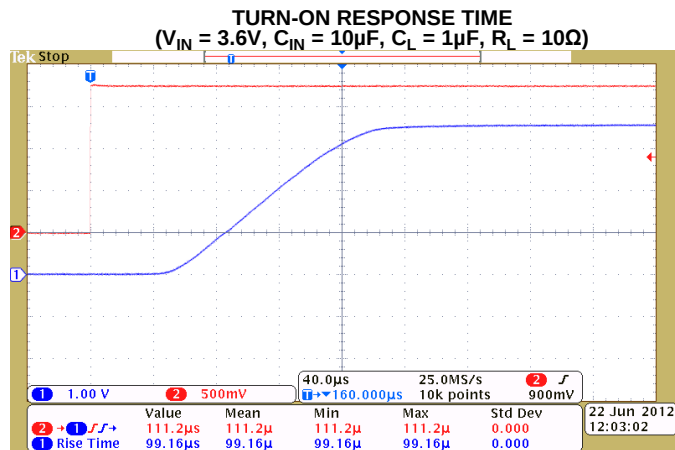
TYPICAL SWITCHING SCOPE CAPTURES AT $T_A = 25^\circ\text{C}$


Figure 24.

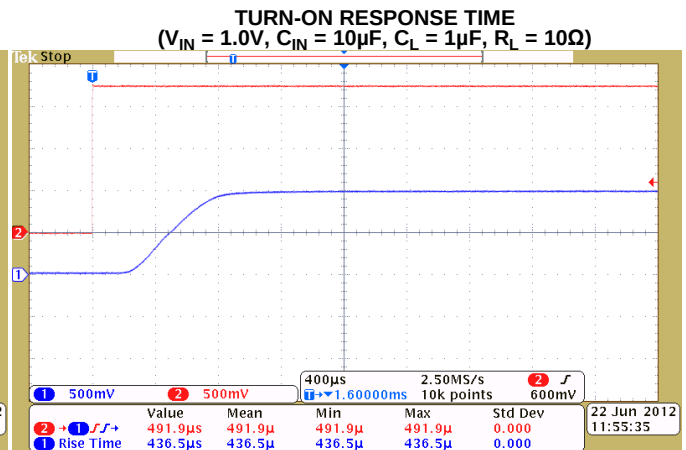


Figure 25.

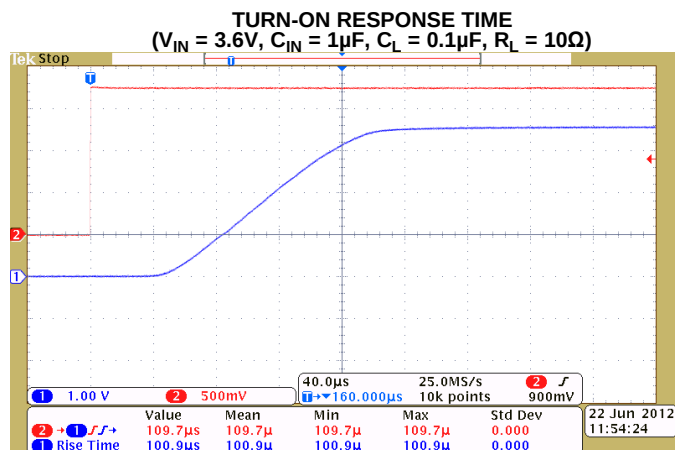


Figure 26.

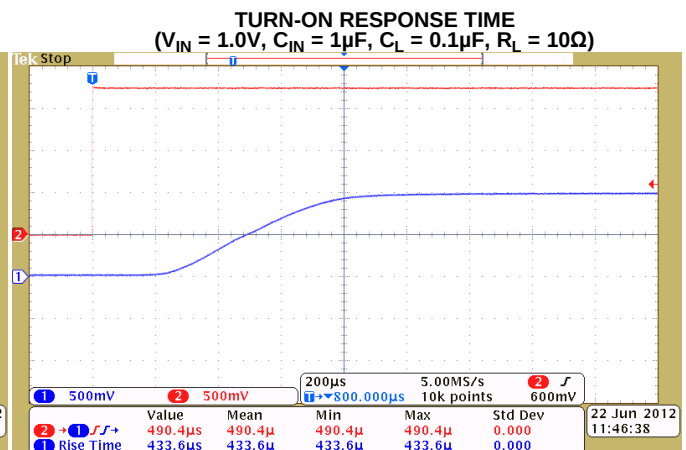


Figure 27.

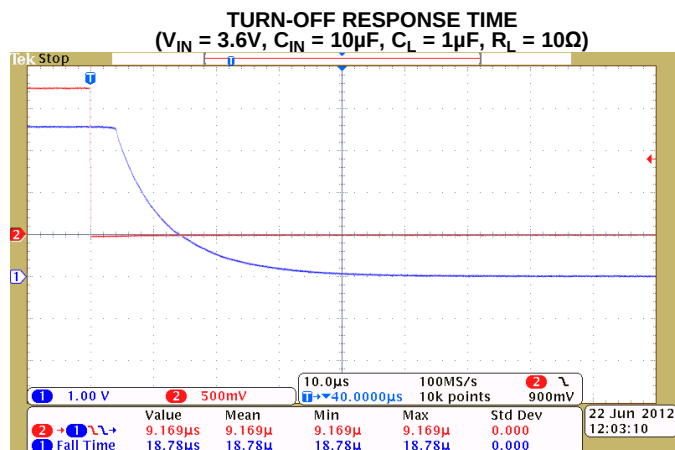


Figure 28.

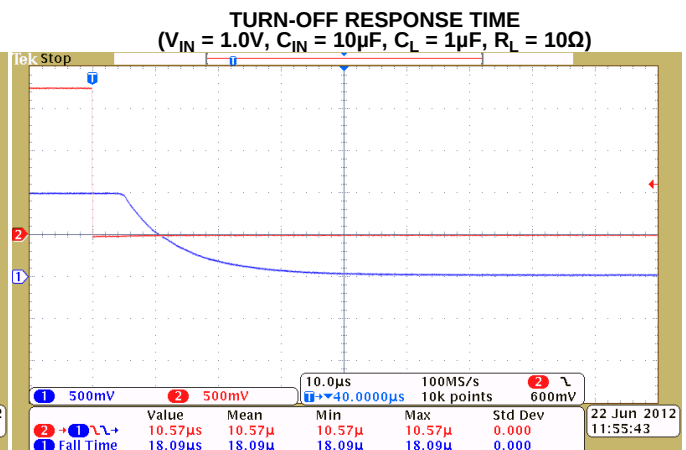


Figure 29.

TYPICAL SWITCHING SCOPE CAPTURES AT $T_A = 25^\circ\text{C}$ (continued)

TURN-OFF RESPONSE TIME

($V_{IN} = 3.6\text{V}$, $C_{IN} = 1\mu\text{F}$, $C_L = 0.1\mu\text{F}$, $R_L = 10\Omega$)

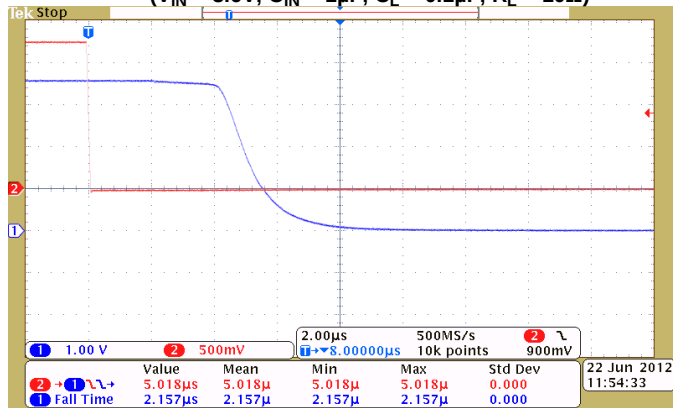


Figure 30.

TURN-OFF RESPONSE TIME

($V_{IN} = 1.0\text{V}$, $C_{IN} = 1\mu\text{F}$, $C_L = 0.1\mu\text{F}$, $R_L = 10\Omega$)

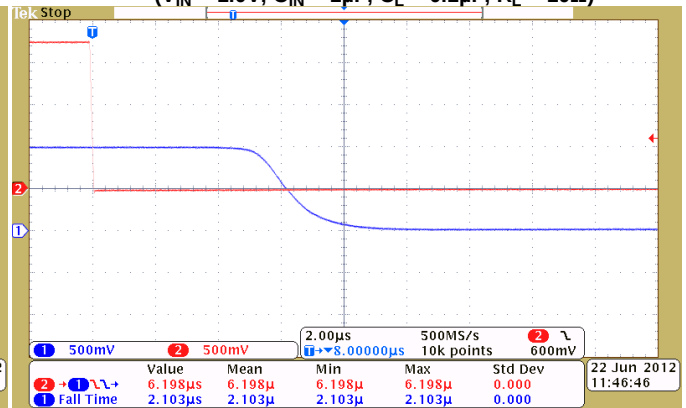


Figure 31.

APPLICATION INFORMATION

ON/OFF CONTROL

The ON pin controls the state of the switch. Asserting ON high enables the switch. ON is active high and has a low threshold making it capable of interfacing with low-voltage signals. The ON pin is compatible with standard GPIO logic threshold. It can be used with any microcontroller with 1.2-V or higher GPIOs.

INPUT CAPACITOR (OPTIONAL)

To limit the voltage drop on the input supply caused by transient inrush currents when the switch turns on into a discharged load capacitor or short-circuit, a capacitor can be placed between V_{IN} and GND. A 1- μ F ceramic capacitor, C_{IN} , placed close to the pins, is usually sufficient. Higher values of C_{IN} can be used to further reduce the voltage drop during high-current application. When switching heavy loads, it is recommended to have an input capacitor about 10 times higher than the output capacitor to avoid excessive voltage drop.

OUTPUT CAPACITOR (OPTIONAL)

Due to the integrated body diode of the PMOS switch, a C_{IN} greater than C_L is highly recommended. A C_L greater than C_{IN} can cause V_{OUT} to exceed V_{IN} when the system supply is removed. This could result in current flow through the body diode from V_{OUT} to V_{IN} . A C_{IN} to C_L ratio of at least 10 to 1 is recommended for minimizing V_{IN} dip caused by inrush currents during startup; however, a 10 to 1 ratio for capacitance is not required for proper functionality of the device. A ratio smaller than 10 to 1 (such as 1 to 1) could cause slightly more V_{IN} dip at turn on due to inrush currents.

BOARD LAYOUT

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal operation. Using wide traces for V_{IN} , V_{OUT} , and GND helps minimize the parasitic electrical effects along with minimizing the case to ambient thermal impedance.

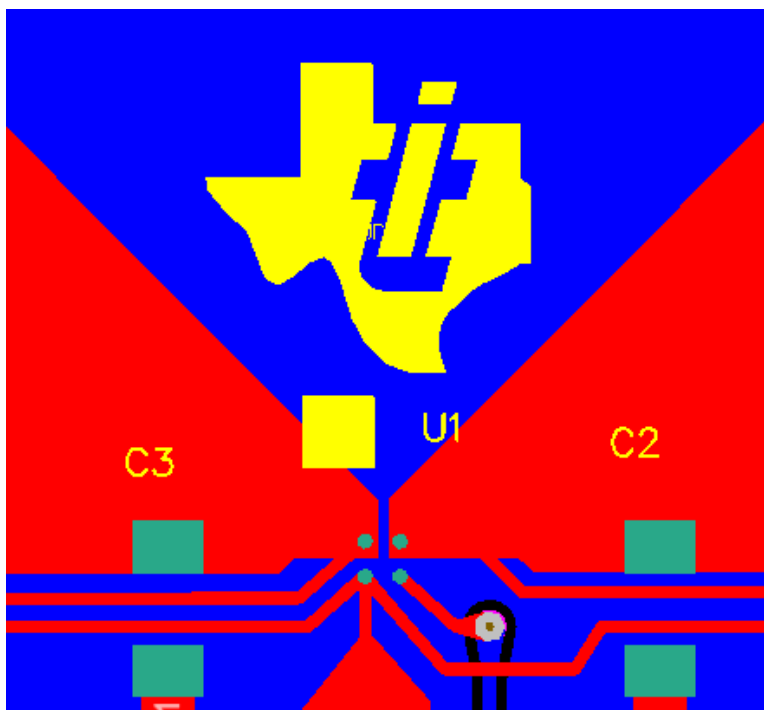


Figure 32. Layout

REVISION HISTORY

Changes from Revision A (August 2012) to Revision B	Page
• 更新了特性。	1
• Added Layout graphic.	14

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TPS22908YZTR	Active	Production	DSBGA (YZT) 4	3000 LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	AT
TPS22908YZTT	Active	Production	DSBGA (YZT) 4	250 SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	AT

⁽¹⁾ **Status:** For more details on status, see our [product life cycle](#).

⁽²⁾ **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

⁽⁴⁾ **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

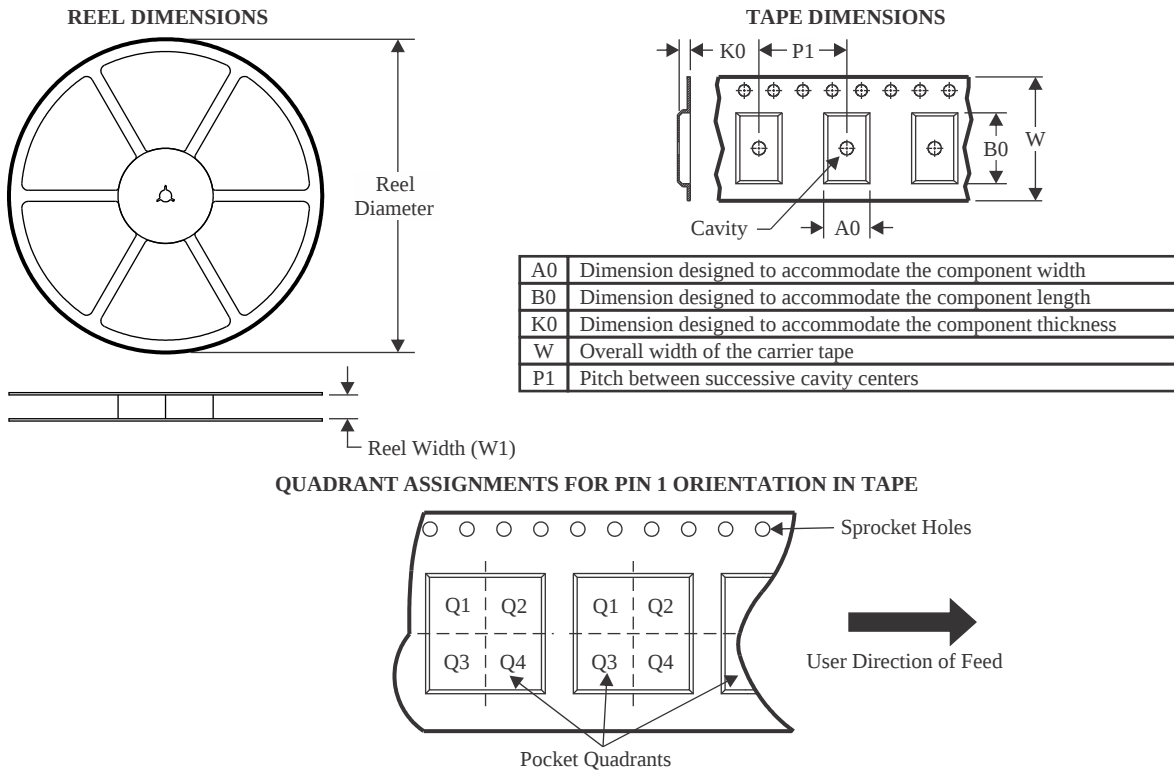
⁽⁶⁾ **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "-" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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TAPE AND REEL INFORMATION



*All dimensions are nominal

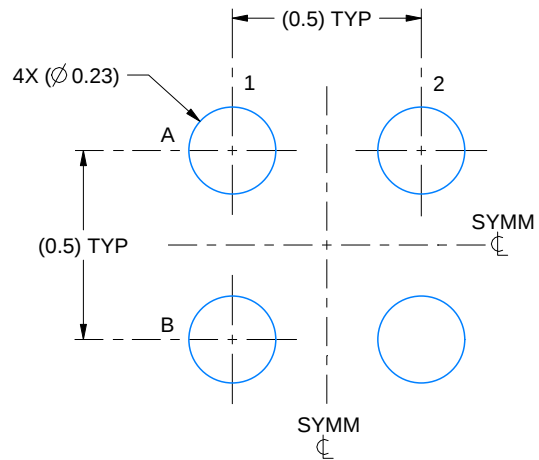
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS22908YZTR	DSBGA	YZT	4	3000	180.0	8.4	0.99	0.99	0.69	4.0	8.0	Q1
TPS22908YZTT	DSBGA	YZT	4	250	180.0	8.4	0.99	0.99	0.69	4.0	8.0	Q1

TAPE AND REEL BOX DIMENSIONS

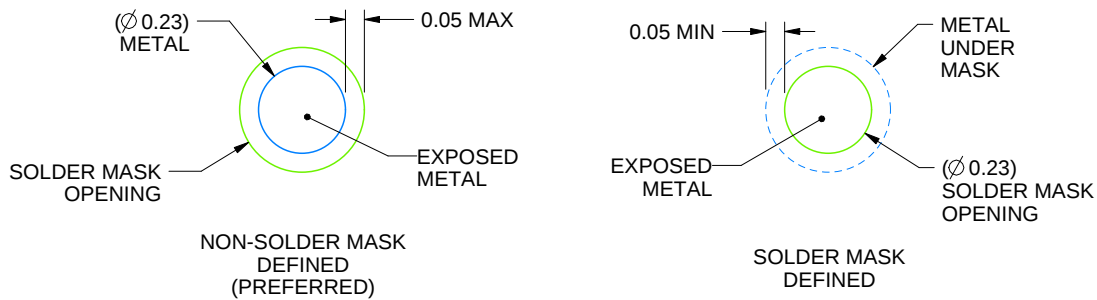


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS22908YZTR	DSBGA	YZT	4	3000	182.0	182.0	20.0
TPS22908YZTT	DSBGA	YZT	4	250	182.0	182.0	20.0



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE:50X



SOLDER MASK DETAILS
NOT TO SCALE

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NOTES: (continued)

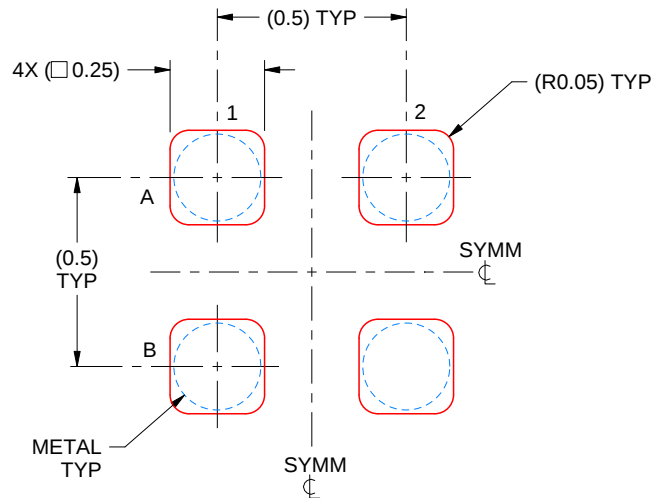
- Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. Refer to Texas Instruments Literature No. SNVA009 (www.ti.com/lit/snva009).

EXAMPLE STENCIL DESIGN

YZT0004

DSBGA - 0.625 mm max height

DIE SIZE BALL GRID ARRAY



SOLDER PASTE EXAMPLE
BASED ON 0.1 mm THICK STENCIL
SCALE:50X

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NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.

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